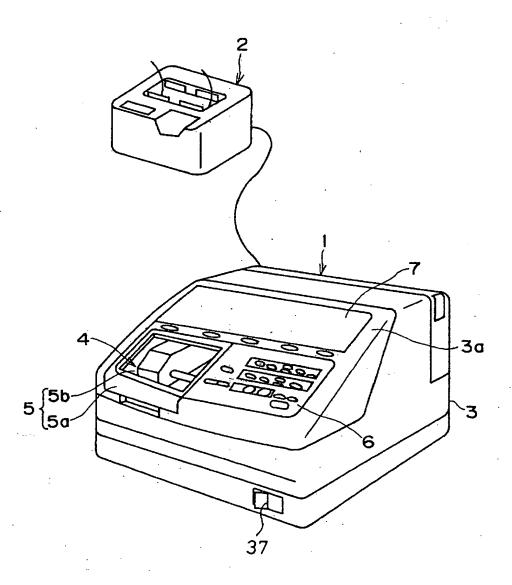
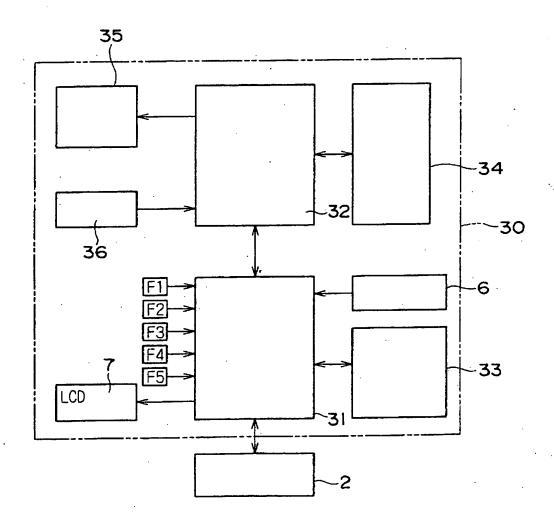
F | G. 1

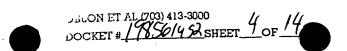


99× **6**d φ 53 CHANGE FRAME CHANGE INPUT ROTATE LENS 2 Move CHAMFER MIRROR STOP <u>m</u>. ~20 612 REFINISH TRIAL **▼** 0 — (FRAME) MIRROR 27 $\underline{\alpha}$ RIGHT MACHINING 23 4 0 $\underline{\omega}$ LENS LEFT 22 ó Switch Input Start ပွဲ OPEN 26 DATA REQUEST Clamp CLOSE 25

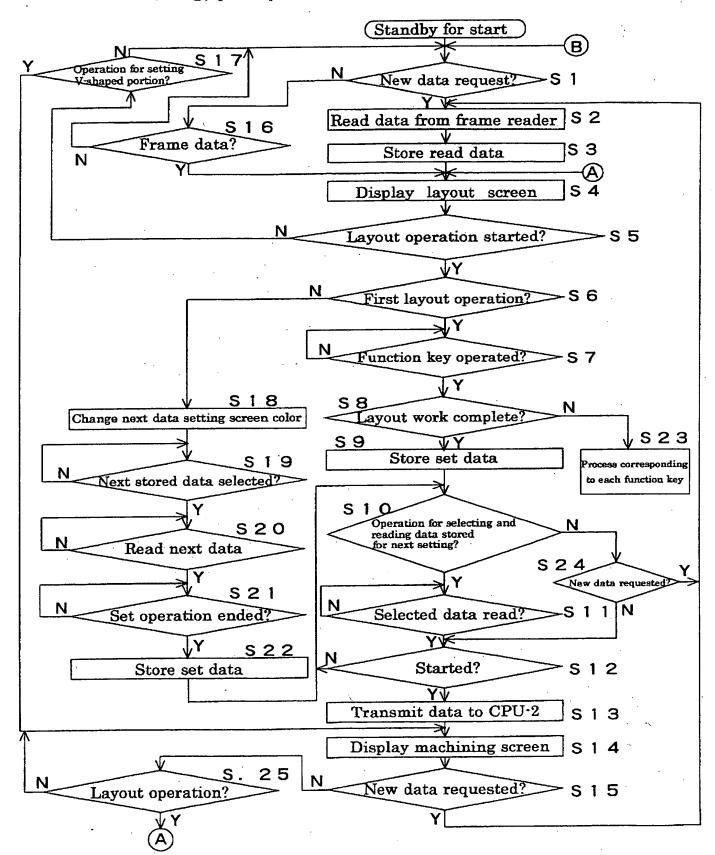
DOCKET #199961452 SHEET OF 14

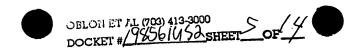
FIG. 3



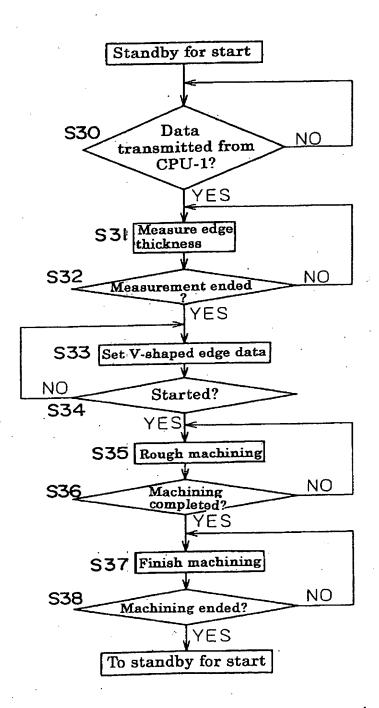


F I G. 4

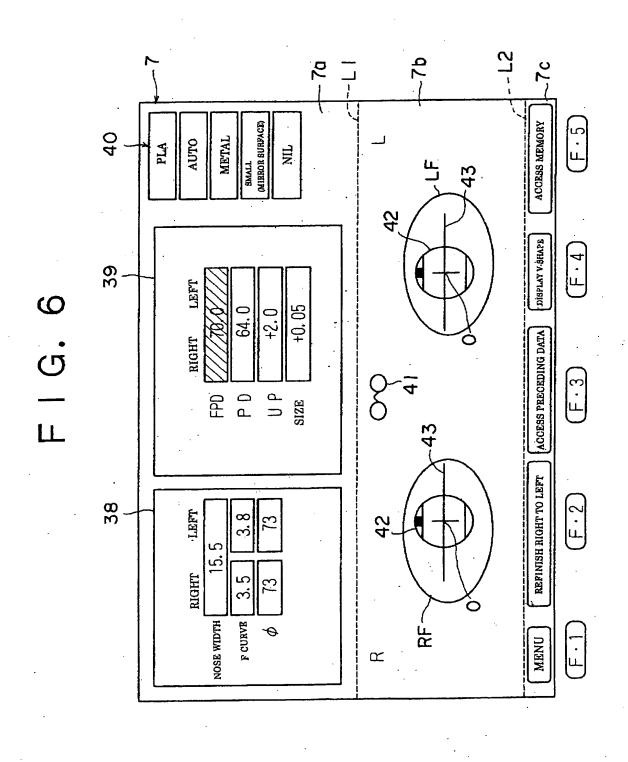


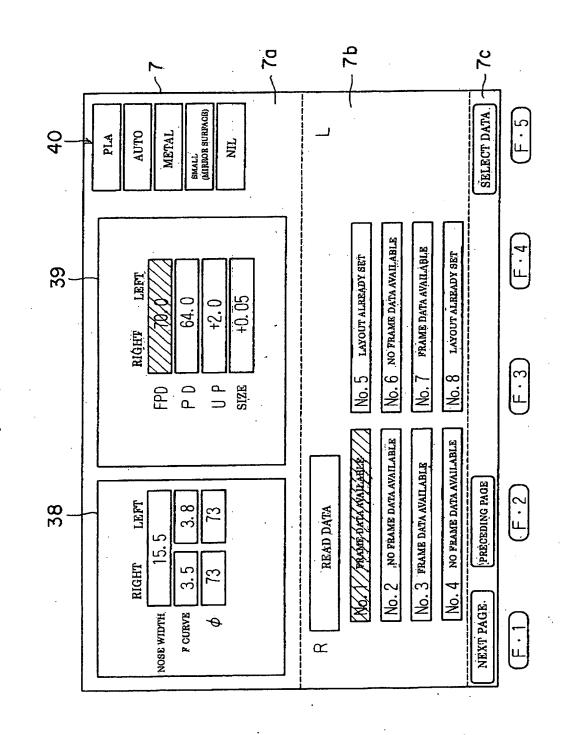


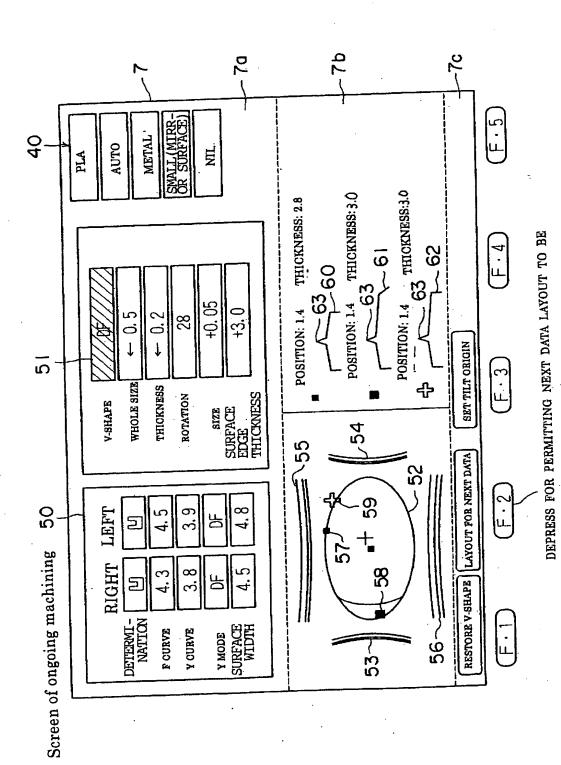
F | G. 5



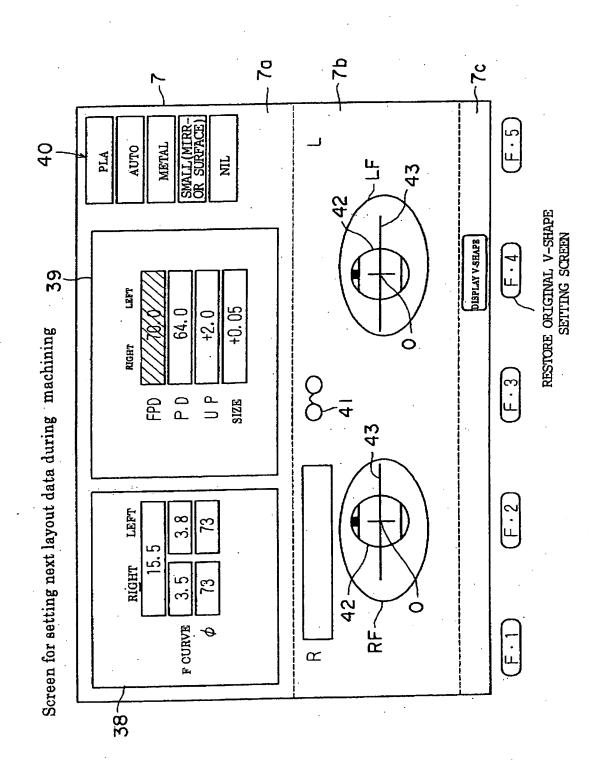
OBLON ET AL (703) 413-3000 DOCKET # 19856/452 SHEET OOF 4





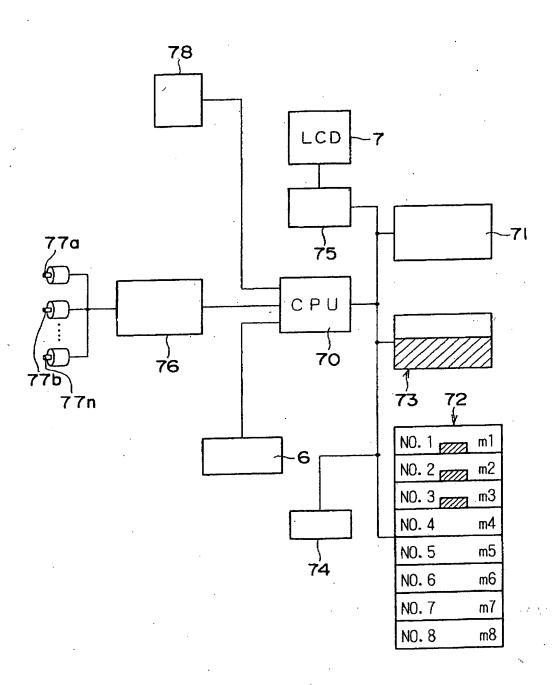


SET DURING MACHINING



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F | G. 10

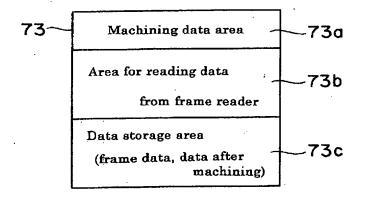


DBLON ET II. (703) 413-3000

DOCKET #/98 56/452 SHEET /OF/4

F | G. 11

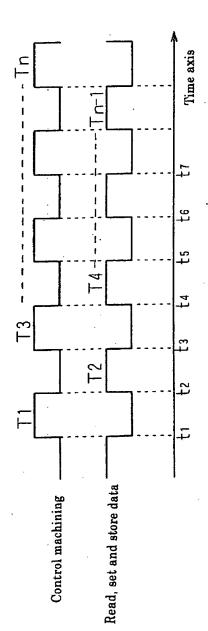
Example of memory area allocation



Read data from frame reader Access data in memory Machining **S44** 345 **S43** YES YES YES Data read from frame reader? Data in memory accessed? Standby for start Machining starter 9 9 **S41**

F | G. 12





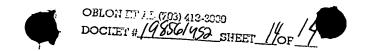


FIG. 14

